

INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 67161-057		SERIAL NO. 10/623565	
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				U.S. PATENT DOCUMENTS			
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codez (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	A1	US 2003/0090935	05/15/2003	HIDAKA			
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number + -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
VTN		JP 2002-117684	04/19/2002	PERNER, et al.			abstract
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
VTN		SCHEUERLEIN, et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell" IEEE International Solid-State Circuits Conference (2000) TA 7.2.					
VTN		DURLAM, et al. "Nonvolatile RAM Based on Magnetic Tunnel Junction Elements" IEEE International Solid-State Circuits Conference (2000) TA 7.3.					
VTN		NAJI, et al. "A 256kb 3.0V 1T1MJT Nonvolatile Magnetoresistive RAM" IEEE International Solid-State Circuits Conference (2001) 7.6.					
EXAMINER		TA		DATE CONSIDERED 9/17/04			

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